

# Abstracts

## UTSi(R) phase locked loops: production 2 GHz RF CMOS

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*R.E. Reedy. "UTSi(R) phase locked loops: production 2 GHz RF CMOS." 1999 Radio Frequency Integrated Circuits (RFIC) Symposium 99. (1999 [RFIC]): 159-162.*

A CMOS technology which is in production of 1 and 2 GHz RF PLLs is presented. Device performance is discussed, along with measured product performance. Advantages of the technology are discussed for application to integration of full RF systems on a single chip.

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